

1 RC Circuits

In this problem, we will be using differential equations to find the voltage across a capacitor over time in an RC circuit. We set up our problem by first defining three functions over time: $I(t)$ is the current at time t , $V(t)$ is the voltage across the circuit at time t , and $V_C(t)$ is the voltage across the capacitor at time t .

Recall from 16A that the voltage across a resistor is defined as $V_R = RI_R$ where I_R is the current across the resistor. Also, recall that the voltage across a capacitor is defined as $V_C = \frac{Q}{C}$ where Q is the charge across the capacitor.

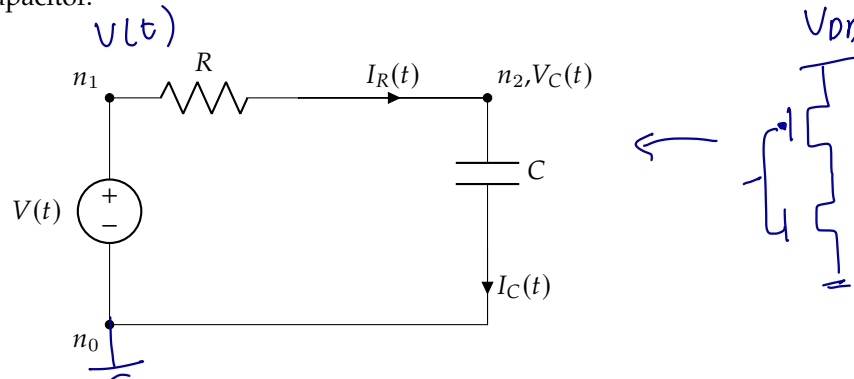


Figure 1: Example Circuit

- a) First, find an equation that relates the current through the capacitor $I_C(t)$ with the voltage across the capacitor $V_C(t)$.

Know: $Q = C \cdot V_C$, $I = \frac{dQ}{dt}$

$$\frac{dQ}{dt} = C \frac{dV_C}{dt} \rightarrow I_C = C \frac{dV_C}{dt}$$

- b) Using nodal analysis, write a differential equation for the capacitor voltage $V_C(t)$. Note that this is also the voltage for the node n_2 .

1. Identify Nodes

Done.

2. Write KCL

$$I_R = I_C$$

3. I/V Relations

$$I_R = \frac{V(t) - V_C(t)}{R} = C \frac{dV_C}{dt} = I_C$$

$$C \frac{dV_C}{dt} = -\frac{1}{R} V_C(t) + \frac{V(t)}{R}$$

4. Simplify

$$\frac{dV_C}{dt} + \frac{1}{RC} V_C(t) = \frac{V(t)}{RC}$$

- c) Let's suppose that at $t = 0$, the capacitor is charged to a voltage V_{DD} ($V_C(0) = V_{DD}$). Let's also assume that $V(t) = 0$ for all $t \geq 0$.

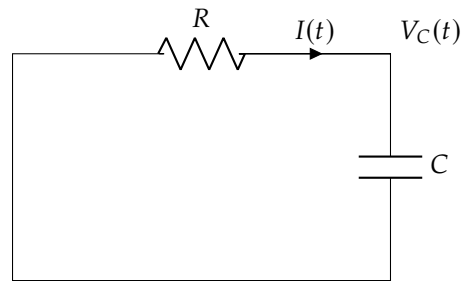


Figure 2: Circuit for part (d)

$$V_C(0) = V_{DD}$$

$$V(t) = 0 \text{ for all } t \geq 0$$

Solve the differential equation for $V_C(t)$ for $t \geq 0$.

Know: $\frac{dV_C}{dt} + \underbrace{\frac{1}{RC}}_a V_C(t) = \frac{V(t)}{RC} = 0$ $b = 0$

$$V_C(0) = V_{DD}$$

Given $\frac{dx}{dt} + ax = 0$

$$X(t) = X(0)e^{-at}$$

Plugging in $a = \frac{1}{RC}$, $X(0) = V_{DD}$, we see that

$$V_C(t) = V_{DD} e^{-\frac{1}{RC} \cdot t}$$

- d) Now, let's suppose that we start with an uncharged capacitor $V_C(0) = 0$. We apply some constant voltage $V(t) = V_{DD}$ across the circuit. Solve the differential equation for $V_C(t)$ for $t \geq 0$.

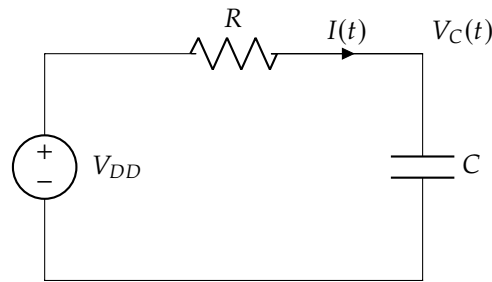


Figure 3: Circuit for part (e)

$$V(t) = V_{DD} \text{ for all } t$$

$$\frac{dV_C}{dt} + \underbrace{\frac{1}{RC}}_a V_C(t) = \frac{V(t)}{RC} = \underbrace{\frac{V_{DD}}{RC}}_b \quad V_C(0) = 0$$

$$\begin{aligned} x(t) &= \left(x(0) - \frac{b}{a} \right) e^{-at} + \frac{b}{a} \\ &= x(0) e^{-at} - \frac{b}{a} (e^{-at} - 1) \\ &= (0) \cdot e^{-\frac{1}{RC}t} - \frac{\frac{V_{DD}}{RC}}{\frac{1}{RC}} (e^{-\frac{1}{RC}t} - 1) \\ &= 0 - V_{DD} (e^{-t/RC} - 1) \\ &= V_{DD} (1 - e^{-t/RC}) \end{aligned}$$

Note: There's an alternate method to solving diff-egs called substitution of vars.

- d) Now, let's suppose that we start with an uncharged capacitor $V_C(0) = 0$. We apply some constant voltage $V(t) = V_{DD}$ across the circuit. Solve the differential equation for $V_C(t)$ for $t \geq 0$.

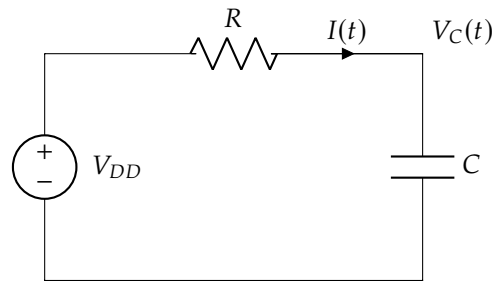


Figure 3: Circuit for part (e)

Alternate Sol:

$$\frac{dV_C}{dt} = -\frac{1}{RC} V_C + \frac{V_{DD}}{RC}$$

$$V_C(0) = 0$$

Define a new variable

$$x = V_C - V_{DD}$$

Then

$$\frac{dx}{dt} = \frac{dV_C}{dt}$$

since V_{DD} is constant

and
plug in x

$$-\frac{1}{RC} x = -\frac{1}{RC} (V_C - V_{DD})$$

This means $\frac{dV_C}{dt} = -\frac{1}{RC} V_C + \frac{V_{DD}}{RC}$ can be rewritten as

$$\frac{dx}{dt} = -\frac{1}{RC} x. \quad \text{This has solution } x(t) = x(0) e^{-\frac{1}{RC} t}$$

$$x(0) = V_C(0) - V_{DD} = -V_{DD} \quad \text{so } x(t) = -V_{DD} e^{-t/RC}$$

Lastly change variables back to V_C .

$$V_C = x + V_{DD} = V_{DD} - V_{DD} e^{-t/RC}$$

2 Graphing RC Responses

Consider the following RC Circuit with a single resistor R , capacitor C , and voltage source $V(t)$.

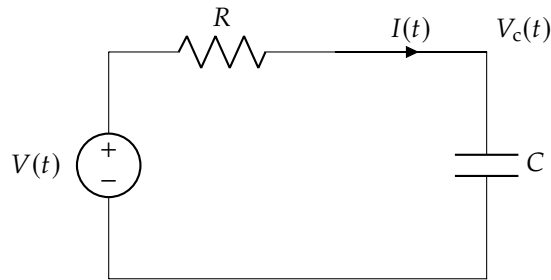
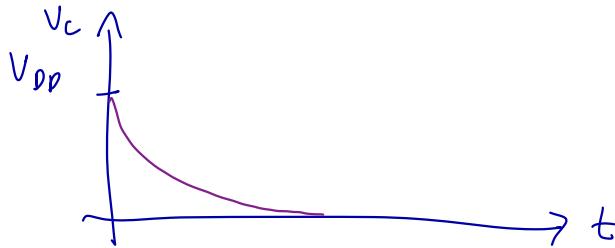


Figure 4: Example Circuit

- a) Let's suppose that at $t = 0$, the capacitor is charged to a voltage V_{DD} ($V_c(0) = V_{DD}$) and that $V(t) = 0$ for all $t \geq 0$. Plot the response $V_c(t)$.

Recall that $V_c(t) = V_{DD} e^{-\frac{1}{RC} t}$

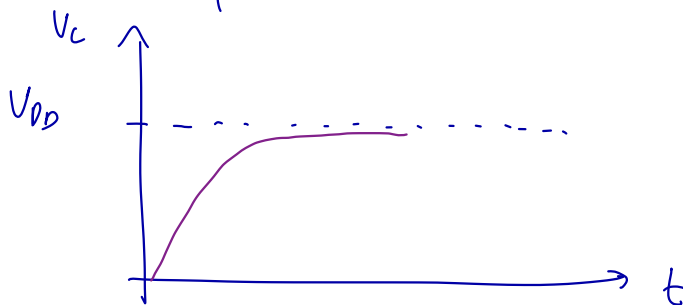
We can plot this on a graphing calculator



- b) Now let's suppose that at $t = 0$, the capacitor is uncharged ($V_c(0) = 0$) and that $V(t) = V_{DD}$ for all $t \geq 0$. Plot the response $V_c(t)$.

Recall that $V_c(t) = V_{DD} (1 - e^{-\frac{1}{RC} t})$.

The plot is shown below



To better understand our responses, we now define a **time constant** which is a measure of how long it takes for the capacitor to charge or discharge. Mathematically, we define τ as the time at which $V_C(\tau)$ is $\frac{1}{e} = 36.8\%$ away from its steady state value.

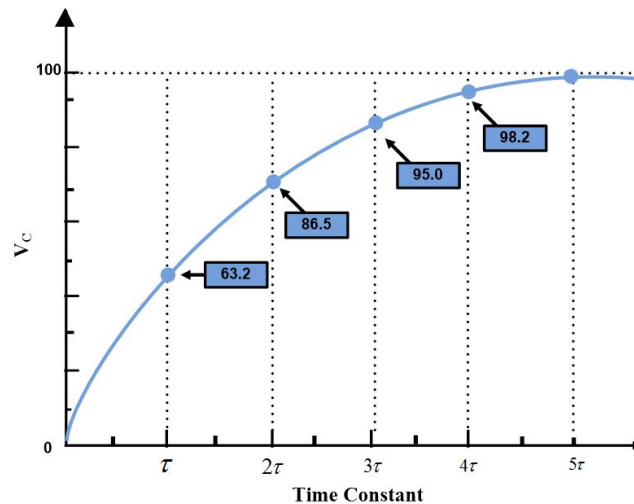


Figure 5: Different values of capacitor voltage at different times, relative to τ .

- c) Suppose that $V_{DD} = 5\text{ V}$, $R = 100\ \Omega$, and $C = 10\ \mu\text{F}$. What is the time constant τ for this circuit?

Let's take the discharging case from part (a).

By definition τ is the time at which $V_C(\tau) = \frac{V_{DD}}{e}$.

We can solve for τ as follows:

$$V_C(\tau) = V_{DD} e^{-\tau/RC} = \frac{V_{DD}}{e}$$

$$e^{-\tau/RC} = \frac{1}{e}$$

$$-\tau/RC = \ln\left(\frac{1}{e}\right) = -1 \rightarrow \tau = RC = 1\text{ ms}$$

or 0.001 s.

- d) Going back to part (b), on what order of magnitude of time (nanoseconds, milliseconds, 10's of seconds, etc.) does this circuit settle (V_C is $> 95\%$ of its value as $t \rightarrow \infty$)?

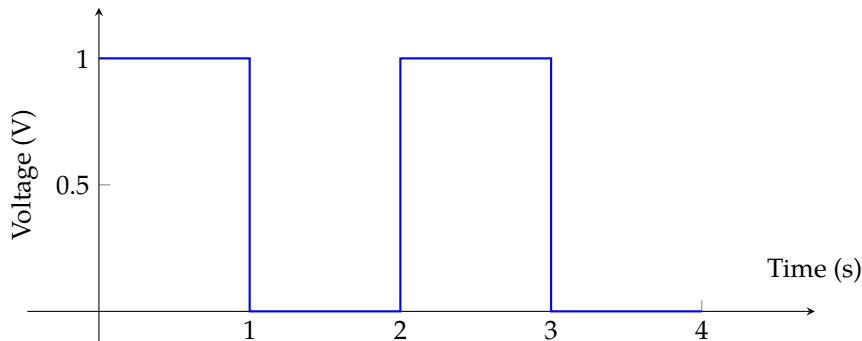
Looking at the graph above, it will take 3τ to reach 95% of the steady state value.

Since $\tau = RC = 1\text{ ms}$, it will take 3 ms to reach within $0.95 V_{DD}$.

- e) Give 2 ways to reduce the settling time of the circuit if we are allowed to change one component in the circuit.

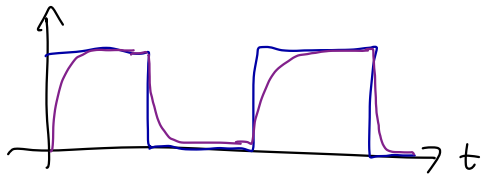
$\tau = RC$. To reduce the time constant, we should either decrease R or decrease C .

- f) Suppose we have a source $V(t)$ that alternates between 0 and $V_{DD} = 1$ V. Given $RC = 0.1$ s, plot the response V_c if $V_c(0) = 0$.



$V(t)$ alternates between 1 and 0. However, it stays constant from $[0, 1)$, $[1, 2)$, ... Therefore, we can solve the differential equations assume $V(t)$ is constant over an interval.

Plot will look like this



Note that $\tau = 0.1$ s and we wait 10τ before switching $V(t)$.

- g) Now suppose we have the same source $V(t)$ but $RC = 1$ s, plot the response V_c if $V_c(0) = 0$.

Here $\tau = 1$ s meaning after 1 s we will only reach 63% of V_{DD} . Note that $63\% = 1 - \frac{1}{e}$.

